

## **IN THE SPECIFICATION**

Please add the following paragraph after paragraph [0020]:

[0020.1] FIG. 5 shows a flowchart of one embodiment of a method in accordance with the present invention.

Please add the following paragraph after paragraph [0038]:

[0038.1] FIG. 5 illustrates a method for optimizing power efficiency in a transmitter apparatus in accordance with the present invention.

Please replace paragraph [0034] with the following:

[0034] FIG. 3-2 illustrates an alternate embodiment of the transmitter apparatus 100 of the present invention. In this embodiment, the first mapper 115 and the summer 175 of the embodiment of FIG. 2-1 are eliminated. The second mapper 120, with adjustments to its stored array, uses the average transmit power indicator "p" 170 as its input. In this case, the "X" signal 180 is determined as follows: for a given average transmit power indicator "p" 170, the corresponding optimal "X" signal 180 is generated that provides the best transmitter power efficiency while satisfying out of band spurious emissions and rho requirements. As in the previous embodiment, these signals that are stored in the second mapper's 120 array are determined by experimentation. The remainder of the embodiment of FIG. 3-2 is the same as the embodiment of FIG. 2-1 and is not discussed further.

Please replace paragraph [0035] with the following:

[0035] FIG. 4-3 illustrates yet another embodiment of the transmitter apparatus 100 of the present invention. This embodiment uses multiple "X" signals that are provided to adjust multiple parts of the transmitter 100 or multiple parameters of the same part of the transmitter 100, instead of just one "X" signal 180 to adjust only one parameter of the power amplifier 140. The multiple "X" signals may be implemented by an array of second mappers 120 and 121, D/A converters 185 and 186, and low pass filters 190 and 191. Each individual element of "X" is determined as described in the above embodiment: to provide the best transmitter power efficiency while still satisfying out of band spurious emissions and rho requirements.

Please replace paragraph [0037] with the following:

[0037] The block diagram of FIG. 43 shows only 2 second mapper functions 120 and 121. However, the present invention is not limited to any one quantity of second mappers. Further, the present invention is not meant to be limited to X being an input to the power amplifier 140, but instead can be used within other parts of transmitter 100.

Please replace paragraph [0038] with the following:

[0038] FIG. 54 illustrates yet another embodiment of the transmitter apparatus 100 of the present invention. Alternatively, the second mapper 120 may also be implemented by analog means and placed after the D/A converter 185. The functionality of the blocks remains the same as do